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2 A dynamic binary instrumentation engine for the ARM architecture

Kim Hazelwood, Artur Klauser

October 2006 CASES '06: Proceedings of the 2006 international conference embedded systems

Publisher: ACM

Full text available: Pdf (455.03 KB)

Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 90, Downlo

Dynamic binary instrumentation (DBI) is a powerful technique for analy numerous DBI frameworks have been developed for general-purpose ar embedded architectures has been ...

**Keywords**: binary instrumentation, dynamic translation, embedded arc

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October 2008 Communications of the ACM

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6 Incremental Support Vector Learning: Analysis, Implementation and Pavel Laskov, Christian Gehl, Stelan Krüger, Klaus-Robert Müller

December 2006 The Journal of Machine Learning Research, Volume 7 Publisher: MIT Press

Full text available: Pdf (297.83 KB) Additional Information: full citation, abs

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Incremental Support Vector Machines (SVM) are instrumental in practical focuses on the design and analysis of efficient incremental SVM learning stable and robust ...

7 Beyond streams and graphs: dynamic tensor analysis

Jimeng Sun, Dacheng Tao, Christos Faloutsos

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How do we find patterns in author-keyword associations, evolving over customer sales information? Matrix decompositions, like principal compositions invaluable tools for mining, ...

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Roger L. Wainwright, Hisham M. Haddad

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Welcome to the 23rd Annual ACM Symposium on Applied Computing (S. to computer scientists, engineers, and practitioners seeking innovative i This year, the conference ...

A regulated transitive reduction (RTR) for longer memory race record Min Xu, Mark D. Hill, Rastislav Bodik

November 2006 ASPLOS-XII: Proceedings of the 12th international conference languages and operating systems

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Now at VMware. Multithreaded deterministic replay has important applic intrusion analysis. Memory race recording is a key technology for multit considerably ...

**Keywords**: determinism, multithreading, race recording

Also published in:

October 2006 SIGARCH Computer Architecture News Volume 34 Issue 5 October 2006 **SIGOPS Operating Systems Review** Volume 40 Issue 5 November 2006 SIGPLAN Notices Volume 41 Issue 11

10 Program locality analysis using reuse distance

Yutao Zhong, Xipeng Shen, Chen Ding August 2009 Transactions on Programming Languages and Systems

Publisher: ACM Request Permissions Full text available: Pdf (2.15 MB)

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Bibliometrics: Downloads (6 Weeks): 48, Downloads (12 Months): 333, Downloads

On modern computer systems, the memory performance of an applicati execution, locality-correlated measures like average miss rate or working distance-the ...

Keywords: Program locality, reuse distance, stack distance, training-ba

11 The Conquest file system: Better performance through a disk/persist An-I Andy Wang, Geoif Kuenning, Peter Reiher, Gerald Popek

August 2006 Transactions on Storage (TOS), Volume 2 Issue 3

Publisher: ACM Sequest Permissions Full text available: Pdf (1.34 MB)

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Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 151, Downloads

Modern file systems assume the use of disk, a system-wide performanc caching and RAM file systems either impose high overhead to access me achieve data persistence ...

**Keywords**: Persistent RAM, file systems, performance measurement, s

12 Practical escape analyses: how good are they?

💫 Kyungwoo Lee, Xing Fang, Samuel P. Midkiff

VEE '07: Proceedings of the 3rd international conference on ' June 2007

Publisher: ACM Pequest Permissions Full text available: 1626.23 KB)

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A key analysis developed for the compilation of parallel programs is three escape analysis), which determines what objects are accessed in more t program ...

Keywords: Java, analysis precision, dynamic compilation, escape analy

13 Traffic modeling and proportional partial caching for peer-to-peer sys Mohamed Hefeeda, Osama Saleh

December 2008 IEEE/ ACM Transactions on Networking (TON), Volume

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Bibliometrics: Downloads (6 Weeks): 20, Downloads (12 Months): 189, Downl

Peer-to-peer (P2P) file sharing systems generate a major portion of the increase in the future. We explore the potential of deploying proxy cach with the goal ...

Keywords: internet measurement, network protocols, peer-to-peer sys

14 The LOCKSS peer-to-peer digital preservation system

Petros Maniatis, Mema Roussopoulos, T. J. Giuli, David S. H. Rosenthal, Ma February 2005 Transactions on Computer Systems (TOCS), Volume 23 Is

Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks): 21, Downloads (12 Months): 156, Downl

The LOCKSS project has developed and deployed in a world-wide test a journals and other archival information published on the Web. It consist persistent Web caches ...

Keywords: Rate limiting, digital preservation, replicated storage

The quest for scalable support of data-intensive workloads in distribution Raicu, Ian T. Foster, Yong Zhao, Philip Little, Christopher M. Moretti, June 2009

HPDC '09: Proceedings of the 18th ACM international sympo computing

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Data-intensive applications involving the analysis of large datasets ofter resources, for which data locality can be crucial to high throughput and approach that ...

Keywords: data diffusion, data management, data-aware scheduling, f

16 Managing prefetch memory for data-intensive online servers Chuanpeng Li, Kai Shen

December 2005 FAST'05: Proceedings of the 4th conference on USENI Technologies - Volume 4, Volume 4

Publisher: USENIX Association

Additional Information: full citation, abstract, references, cited by, index terms

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Data-intensive online servers may contain a significant amount of prefer

I/O prefetching and high execution concurrency. Using a traditional accereclamation policy, memory ...

17 New results on web caching with request reordering

Susanne Albers

June 2004 SPAA '04: Proceedings of the sixteenth annual ACM symposi architectures

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Full text available: Pdf (186.52 KB)

Additional Information: full citation, abs

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We study web caching with request reordering. The goal is to maintain requests can be served at low cost. To improve cache hit rates, a limiter [6], who recently ...

Keywords: approximation, batch, cache, competitive, document, offlin-

A concurrent dynamic analysis framework for multicore hardware

Jungwoo Ha, Matthew Arnold, Stephen M. Blackburn, Kathryn S. McKinley

October 2009 OOPSLA '09: Proceeding of the 24th ACM SIGPLAN conferen
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Software has spent the bounty of Moore's law by solving harder problen level languages, virtual machine technology, binary rewriting, and dyna more productive and programs ...

Keywords: dynamic analysis, instrumentation, multicore, profiling

Also published in:

October 2009 SIGPLAN Notices Volume 44 Issue 10

Phase-based cache reconfiguration for a highly-configurable two-lever Ann Gordon-Ross, Jeremy Lau, Brad Calder

May 2008 GLSVLSI '08: Proceedings of the 18th ACM Great Lakes sym

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Full text available: Pdf (286.03 KB) Additional Information: full citation, abs

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Phase-based tuning methodologies specialize system parameters for ear are varied during execution, as opposed to remaining fixed as in an app and logic suggests ...

**Keywords**: cache tuning, caches, configurable architecture, configurable reconfiguration, phase-based tuning

20

Towards practical page coloring-based multicore cache managemen Xiao Zhang, Sandhya Dwarkadas, Kai Shen April 2009 **EuroSys '09:** Proceedings of the 4th ACM European conferen

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Modern multi-core processors present new resource management challe simultaneously executing processes sharing on-chip resources (particula demonstrates that the operating system ...

Keywords: cache partitioning, multi-core, page coloring, resource man

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